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Date: August 15, 2006

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Fax No.: (571) 273-8300

Confirmation No. 1091

From: Kenneth W. Fields

Number of pages being transmitted, including this cover sheet: 16

Please direct all questions concerning the transmittal of these pages to Kenneth Fields (202-721-8200).

RE: AMENDMENT for Serial No. 10/773,276 (Tomoo KIMURA et al.), filed February 9, 2004

MESSAGE:

ATTACHED HEREWITH ARE THE FOLLOWING ITEMS:

1. Amendment (15 pages)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Confirmation No. 1091
Tomoo KIMURA et al. : Attorney Docket No. 2004_0156A
Serial No. 10/773,276 : Group Art Unit 2825
Filed February 9, 2004 : Examiner Vuthe Siek
LOGIC CIRCUIT OPTIMIZING METHOD, : Mail Stop: AMENDMENT
LOGIC CIRCUIT OPTIMIZING DEVICE
AND LOGIC CIRCUIT COMPOSING DEVICE

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Sir:

In response to the Office Action of May 15, 2006, please amend the above-referenced
U.S. patent application as follows: